



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Zhao, et al.**

Application Serial No.: **09/317,536**

Filed: **May 24, 1999**

Title: **Interconnect with Low Dielectric
Constant Insulators for
Semiconductor Integrated Circuits**

TRANSMITTAL OF FORMAL DRAWINGS

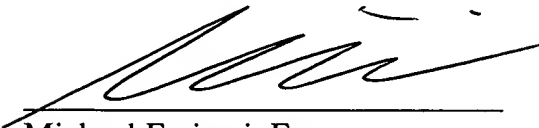
Commissioner for Patents
Official Draftsperson
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find fifteen (15) sheets of formal drawings for the above-identified application, which include amendments to Figures 1A-7B as required by the Notice of Draftsperson's Patent Drawing Review (PTO-948), dated May 24, 1999. Issue fee payment has been submitted under separate cover. An early issuance of the above-identified application is respectfully requested.

Respectfully Submitted;
FARJAMI & FARJAMI LLP

Dated: 3/16/04

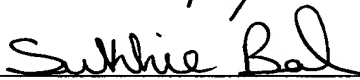

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